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Notice of Allowability	Application No.	Applicant(s)		
	10/750,259 Examiner	SCHOENBORN, THEODORE Z.  Art Unit		
	Edward Raymond	2857		
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. <b>THIS</b>	
1. This communication is responsive to <u>RCE filed 7/24/06.</u>				
2. X The allowed claim(s) is/are 1-4,6-19,24-27 and 29-32.				
<ol> <li>Acknowledgment is made of a claim for foreign priority until a)</li> <li>All b)</li> <li>Some* c)</li> <li>None of the:</li> <li>Certified copies of the priority documents have</li> <li>Certified copies of the priority documents have</li> <li>Copies of the certified copies of the priority documents have</li> <li>International Bureau (PCT Rule 17.2(a)).</li> </ol>	been received. been received in Application No		tion from the	
* Certified copies not received:				
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a reply ENT of this application.	complying with the re	quirements	
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give	itted. Note the attached EXAMINER' es reason(s) why the oath or declara	S AMENDMENT or Nation is deficient.	IOTICE OF	
<ol> <li>CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers</li> <li>hereto or 2) to Paper No./Mail Date</li> <li>including changes required by the attached Examiner's Paper No./Mail Date</li> </ol>	on's Patent Drawing Review ( PTO-			
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the drawir he header according to 37 CFR 1.121(	ngs in the front (not the	e back) of	
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.				
Attachm nt(s) 1. ☐ Notice of References Cited (PTO-892)	5. Notice of Informal P	• •	O-152)	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		<ol> <li>Interview Summary (PTO-413),         Paper No./Mail Date <u>20060729</u>.     </li> <li>Examiner's Amendment/Comment</li> </ol>		
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	7. 🛭 Examiner's Amendr			
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material		8.  Examiner's Statement of Reasons for Allowance		
	9.			

## **DETAILED ACTION**

## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Alan Aldous on July 29, 2001.

The application has been amended as follows:

Replace the text of claim 1 with the following text:

"1. A chip comprising: transmitters to provide transmit signals to chip interfaces, wherein the chip interfaces are to make the transmit signals available external to the chip; voltage control circuitry to control voltages of the transmit signals; receivers to receive external signals from another chip, wherein the external signals include looped back signals of at least portions of the transmit signals and are provided by the other chip to the receivers; and evaluation circuitry to determine whether the transmit signals were usable by the other chip based on an evaluation of at least one of the received external signals and to provide a usability indicating signal to the voltage control circuitry indicative of whether the transmit signals were usable by the other chip and wherein the evaluation circuitry compares at least portions of the looped back signals to at least partial representations of the transmit signals.

Replace the text of claim 15 with the following text:

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"15. A chip comprising: a transmitter to provide a transmit signal to at least one chip interface, wherein each of the chip interfaces is to make the transmit signal available external to the chip; voltage control circuitry to control a voltage of the transmit signal; a receiver to receive an external signal from another chip, wherein the external signals include looped back signals of at least portions of the transmit signals and are provided by the second chip to the receivers,; and evaluation circuitry to determine whether the transmit signal was usable by the other chip based on an evaluation of the received external signal and to provide a usability indicating signal to the voltage control circuitry indicative of whether the transmit signal was usable by the other chip and wherein the evaluation circuitry compares at least portions of the looped back signals to at least partial representations of the transmit signals.

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Replace the text of claim 24 with the following text:

"24. A system comprising: first and second interconnects; first and second chips coupled by the first and second interconnects; wherein the first chip includes: transmitters to provide transmit signals to first interconnects; voltage control circuitry to control voltages of the transmit signals; receivers to receive external signals from the second chip, wherein the external signals include looped back signals of at least portions of the transmit signals and are provided by the second chip to the receivers; and evaluation circuitry to determine whether the transmit signals were usable by the second chip based on an evaluation of at least one of the received external signals and to provide a usability indicating signal to the voltage control circuitry indicative of whether the transmit signals were usable by the second chip and wherein the evaluation

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circuitry compares at least portions of the looped back signals to at least partial representations of the transmit signals."

Claims 5 and 28 are cancelled.

## Allowable Subject Matter

2. Claims 1-4, 6-19, 24-27 and 29-32 are allowed.

## Contact Information

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edward Raymond whose telephone number is 571-272-221. The examiner can normally be reached on M-F 8:30-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Marc Hoff can be reached on 571-272-2216. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

**Edward Raymond** 

Primary Examiner Art Unit 2857

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